



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of:

JEAN-DIDIER ALLEG RUCCI

Application No.: 09/705,487

Filed: November 3, 2000

For: **DIAGNOSTIC SCHEME FOR
PROGRAMMING LOGIC IN A SYSTEM
ON A CHIP**

Art Group: 2181

Examiner: Xuan Marian Thai

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted concurrently with the Request for Continued Examination. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).


The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

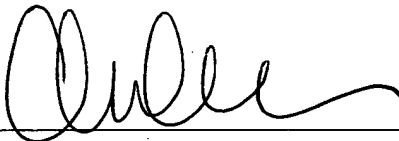
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Substitute for Form 1449/PTO

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

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of

2

Complete if Known

Application Number	09/705,487
Filing Date	November 3, 2000
First Named Inventor:	Jean-Didier Allegrucci
Art Unit	2111
Examiner Name	Xuan Marian Thai
Attorney Docket Number	003242.P015

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If known)				
		us-	5,047,926	9/10/1991	Kuo, et al.	
		us-	5,367,550	11/22/1994	Ishida	
		us-	5,572,667	11/5/1996	Ideta	
		us-	5,710,891	1/20/1998	Normoyle, et al.	RECEIVED
		us-	5,717,851	2/10/1998	Yishay, et al.	
		us-	5,724,505	3/3/1998	Argade, et al.	MAY 17 2004
		us-	5,737,516	4/7/1998	Circello, et al.	
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		us-	6,026,501	2/15/00	Hohl, et al.	
		us-	6,134,652	10/17/00	Warren	
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		us-	6,425,101 B1	7/23/02	Garreau	
		us-	6,430,727 B1	8/6/2002	Warren	
		us-	6,598,178 B1	7/22/2003	Yee, et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴ Kind Code ⁵ (if known)				
		EP	0 636 976 A1	2/1/1995	Klapproth		
		WO	00/22546	4/20/2000	Winegarden, et al.		

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Signature

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Substitute for Form 1449/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	09/705,487
		Filing Date	November 3, 2000
		First Named Inventor:	Jean-Didier Allegrucci
		Art Unit	2111
		Examiner Name	Xuan Marian Thai
		Attorney Docket Number	003242.P015
Sheet	2	of	
NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		Efficient Mechanism for Multiple Debug Modes, IBM Technical Disclosure Bulletin, November, 1995, p. 65-68, Vo. 38, No. 11, Armonk, NY, USA	

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